



इलेक्ट्रॉनिक्स एवं संचार अभियंत्रण विभाग
ELECTRONICS AND COMMUNICATION ENGINEERING DEPT.

राष्ट्रीय प्रौद्योगिकी संस्थान पटना / NATIONAL INSTITUTE OF TECHNOLOGY PATNA
अशोक राजपथ, पटना- 800 005 / ASHOK RAJPATH, PATNA – 800 005, BIHAR

संदर्भ/Ref. : ECE/NITP/ INT/651/24

दिनांक /Date : 12/04/24

Recruitment of Junior Research Fellow (JRF) in the Department of Electronics and Communication Engineering

Applications are invited from Indian nationals for the following position in the project entitled “**Design and development of Audio Sensor and Data Acquisition Systems**” for the consortium project “**ML-Enabled RISC-V based i-LoRa SOC for Forest Event monitoring**” by Ministry of Electronics and Information Technology (MeitY), under **Chips to Start-up (C2S) Programme**, Govt. of India, New Delhi.

Name of the Position	Junior Research Fellow (JRF)
Number of Post	02 (Two)
Duration of the Post	02 (Two) Years, thereafter Senior Research Fellow (SRF) till completion of the project
Consolidated Fellowship/Salary	Rs. 37,000+HRA @ 16 % per month (for JRF) Rs. 42,000+HRA @ 16 % per month (for SRF)
Essential Qualification	B.E./B.Tech. with minimum of 60% marks in Electronic and Communication Engineering and having valid GATE score (or) M.E./M.Tech. with minimum of 60% marks in Electronics and Communication Engineering/VLSI/Embedded System/ Microelectronics/ Electronics System and GATE qualified.
Desirable Requirements	(i) Knowledge of OS (Window/Linux/CentOS/RHEL). (ii) Knowledge of EDA tools (Cadence/Synopsys/Mentor Graphics). (iii) knowledge of Analog/RF/Mixed-mode Circuit Design, HDL Coding.

How to Apply:

Interested and eligible candidates may send the scan copy of application form (annexure-I) along with resume highlighting the relevant exposure and scanned copy of all testimonials to the mail id: balchandnagar@nitp.ac.in on or before **12th May 2024**.

Note:

1. Position is purely temporary and is coterminous with the project.
2. An applicant has to ensure the authenticity of the information provided in support of experience claimed, other documents and photograph (ID proof).
3. Shortlisted candidates and date of interview will be notified via email.
4. No-Objection certificate has to be submitted by the candidates who are already employed.
5. No TA/DA will be paid for attending the interview.
6. The candidate is expected to join immediately, if selected.
7. The selected candidates shall be encouraged to register for Ph.D. in Electronics and Communication Department during July 2024 session.

Balchandnagar
12.04.2024

Dr. Bal Chand Nagar
Chief Investigator
balchandnagar@nitp.ac.in
Contact: +91-9993102487

ANNEXURE-I

NATIONAL INSTITUTE OF TECHNOLOGY PATNA

ASHOK RAJPATH, PATNA—800 005, (BIHAR)

APPLICATION FOR JUNIOR RESEARCH FELLOSHIP

Recent Photo

1.	Name in full (in capital letters)			
2.	Father's Name			
3.	Mother's Name			
4.	Date of birth (Please attach true copy of certificate)			
5.	a) Marital Status:	b) Gender:		
6.	a) Permanent address:	b) Correspondence Address:		
	Phone No. (including STD code)		E-mail ID	
7.	Nationality			
8.	Category under which you are seeking reservation/relaxation			
9.	Were you at any time declared medically unfit; asked to submit your resignation; discharged or dismissed from Govt./PSU/ Autonomous Body or Private Service? If yes, give details in a separate sheet.	Yes/No		

10. Details of Educational Qualification (from Secondary Level):

Exam. Passed	Specialization	Board/University	Passing Year	Class/Division	% marks/ CGPA
10 th					
12 th /equivalent					
UG					
PG					
Others					
GATE /NET /Others					

11. Details of employments:

S.No.	Name and address of Employer	Designation	Pay-scale	Dates		Brief description of duties	Reasons for leaving
				From	To		
1.							
2.							
3.							

APPLICANT'S DECLARATION

"I hereby declare that I fulfil the eligibility conditions to the post and that the statements made by me in form are true, complete and correct to best of my knowledge and belief."

Place:

Date:

Signature of Applicant
Name: